## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

LaSHANNON HYDER

Serial No.: to be assigned

Filed: July 11, 2003

For: LOW V

LOW VOLTAGE LUMINAIRE

**ASSEMBLY** 

## **INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450

Sir:

In accordance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, Applicant brings the attached disclosures to the Examiner's attention and requests that they be considered and made of record in this application. Also attached is a completed PTO-1449 form.

Respectfully submitted,

Marcus R. Mickney

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Attorney for Applicant

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Dated: July 11, 2003

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Substitute	for form 1449A/	PTO	<del></del>	Con	mplete if Known
				Application Number	To be assigned
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)			CLOCUDE	Filing Date	July 11, 2003
				First Named Inventor	LaShannon Hyder
			PPLICANT	Art Unit	
			necessary)	Examiner Name	
Sheet	1	of	1	Attorney Docket Number	45383

U.S. PATENT DOCUMENTS							
Examiner Initials	Cite No.	Document Number  Number-Kind Code (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear		
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FOREIGN PATENT DOCUMENTS							
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NON PATENT LITERATURE DOCUMENTS					
Examiner Initials	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publishers, city and/or country where published	Т		
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